PG DISSERTION (2016-17)

Sr. No	Roll No	Student's Name	Dissertation Title	Guide	External Examiner
1	P15EC001	Navraj Ashok Kaushal	Parametric Analysis of ODM-Index Modulation using MMSE and its extension to MIMO-MRC	Dr. U. D. Dalal	Dr. Arvind Kumar, Assistant ProfessorNIT, Kurukshetra
2	P15EC003	Surya Gupta	An approach to Enhance the IRNSS Signal in the Presence of unintentional Interference	Dr. S. N. Shah	Dr. A. S. Gandhi, Professor, VNIT, Nagpur
3	P15EC004	Harshit Gupta	FPGA Implementation of Face Tracking	Prof. P. J. Engineer	Prof. Arvind Rajawat, Professor, MANIT, Bhopal, MP
4	P15EC005	Khambe Digvijay R.	Design and Implementation of Adaptive Filter for Noise Cancellation using FPGA	Dr. A. D. Darji	Dr. Tanish Zaveri, Professor, ITNU, Ahmedabad
5	P15EC006	Mukul Kumar	Performance Comparison of Precoding Techniques in MU-MIMO System	Dr. Shilpi Gupta	Dr. Arvind Kumar, Assistant Professor NIT, Kurukshetra
6	P15EC007	Parmar Prakash G.	Performance Evaluation of Frequency Scan and LTE-Advanced Carrier Aggregation Throughput	Dr. U. D. Dalal	Dr. A. S. Gandhi, Professor, VNIT, Nagpur
7	P15EC008	Preeti Soni	Robust DWT based Digital Image Watermarking using Compressive Sensing and Singular Value Decomposition	Dr. J. N. Patel	Dr. Suman Mitra, DAIICT, Gandhinagar
8	P15EC009	Badisa Prasanth	Design and Analysis of Different Dispersion Compensation Techniques in Optical Fiber Communication System	Dr. P. N. Patel	Dr. Hitesh B. Pandya, Scientist-SE, ITER India Institute for Plasma Research, Gandhinagar
9	P15EC010	Hind Gaurav	Waveform Analysis for Target Parameters Estimation for MIMO Radar	Dr. Shilpi Gupta	Dr. Arvind Kumar, Assistant Professor, NIT, Kurukshetra
10	P15EC011	Sreejith R.	TSE using Single Reference point to approximate Ionodelay for IRNSS Receiver	Dr. S. N. Shah	Dr. A. S. Gandhi, Professor, VNIT, Nagpur
11	P15EC012	Nitin Kumar	Design and Performance Analysis of WDM Passive Optical Networks	Dr. P. N. Patel	Dr. Hitesh B. Pandya, Scientist-SE, ITER India Institute for Plasma Research, Gandhinagar
12	P15EC013	Virendra Pratap	Performance Analysis of Long Period Fiber Gratings for Sensing Applications	Dr. Shilpi Gupta	Dr. Arvind Kumar, Assistant Professor NIT, Kurukshetra
13	P15EC014	Km. Rashmi Singh	Use of Reflective Semiconductor Optical Amplifiers as a Transmitter at upstream in bidirectional communication in WDM Passive Optical Networks	Dr. A. S. Mandloi	Dr. Hitesh B. Pandya, Scientist-SE, ITER India Institute for Plasma Research, Gandhinagar
14	P15EC015	Parmar Ashaben B.	Optimal Power combination NOMA with LDPC & MIMO	Dr. U. D. Dalal	Dr. A. S. Gandhi, Professor, VNIT, Nagpur
15	P15EC017	Divyeshkumar Z. Patel	PMD and Chromatic Dispersion Compensation in Optical Communication using LPC Coded-OFDM	Dr. A. S. Mandloi	Dr. Arvind Kumar, Assistant Professor NIT, Kurukshetra

16	P15EC018	Vasava Divyaniben J.	Image Registration using Active Contour and Radon Transform	Dr. J. N. Sarvaiya	Dr. Suman Mitra, DAIICT, Gandhinagar
17	P15EC019	Ankita Srivastava	Study and implementation of Link Layer Topology, LE 2M and Long Range Bluetooth Features	Dr. S. N. Shah	Dr. A. S. Gandhi, Professor, VNIT, Nagpur
18	P15VL001	Mansoor Sameer Rafikbhai	Optimized cell design for digital VLSI	Dr. Z. M. Patel	Dr. Biswajit Mishra, Professor, DAIICT, Near Indroda Circle, Gandhinagar
19	P15VL002	Chilukuri Narendra	Timing and Power Optimization for Robust Digital VLSI Design	Prof. M. C. Patel	Dr. M. V. Shah, Professor, L.D. College of Engg., Navrangpura, Ahmedabad
20	P15VL003	Palakayala Ravikiran	Density Evolution of Low Density Parity Check Codes	Prof. M. C. Patel	Dr. M. V. Shah, Professor, L.D. College of Engg., Navrangpura, Ahmedabad
21	P15VL006	Rohit Kumar Singh	FPGA Implementation of Face Detection	Prof. P. J. Engineer	Prof. Arvind Rajawat, Professor, MANIT, Bhopal, MP
22	P15VL007	Abhishek Muni	Bio Signal Conditioning using Along VLSI	Dr. Z. M. Patel	Dr. Amisha Naik, Asso. Professor, Nirma University, Ahmedabad
23	P15VL008	Throvagunta Brahmaiah	Design of a High Speed Low Power Flash ADC by Employing Binary Search Algorithm in 14nm technology	Prof. P. K. Shah	Dr. V. S. Tripathi, Asso. Professor, MNIT, Allahabad
24	P15VL009	Kolusu Siva Mounica	Power Optimization Techniques for High Speed Processor Core in SUB 14nm Technology Node	Prof. P. K. Shah	Dr. V. S. Tripathi, Asso. Professor, MNIT, Allahabad
25	P15VL010	Syam Sanal	Implementation of Multi Threaded Image Processing using Recon OS on Reconfigurable Computing System	Prof. P. J. Engineer	Prof. Arvind Rajawat, Professor, MANIT, Bhopal, MP
26	P15VL012	Naga Lingeswara Rao P	FPGA implementation of programmable DPLL for bio-impedance modulator.	Dr. R. N. Dhavse	Dr. Biswajit Mishra, Professor, DAIICT, Near Indroda Circle, Gandhinagar
27	P15VL013	Shraddha Rathore	Area Efficient Layout of a 4-bit Flash ADC Using SCL 180nm Technology Library	Dr. A. D. Darji	Dr. Nitin S. Kale, Co-Founder & Chief Technology Officer, Nano Sniff Technology Pvt. Ltd., SINE 3rd Floor, CSRE Building, IIT, Mumbai – 400 076
28	P15VL014	Mahesh	Design and Implementation of Programmable Digital Low Pass Filter for Biomedical Applications	Dr. R. N. Dhavse	Dr. Amisha Naik, Asso. Professor, Nirma University, Ahmedabad
29	P15VL015	Abhilash Satish Ramteke	HDL hardware implementation of LDPC codes	Dr. Z. M. Patel	Dr. Biswajit Mishra, Professor, DAIICT, Near Indroda Circle, Gandhinagar
30	P15VL016	Howal Prashant Sunil	Design and Implementation of Floating Point Vedic Multiplier in VHDL and its Applications	. P. Upla, Prof. M. C. I	Dr. Jignesh Bhatt, IIT Vadodara
31	P15VL017	Mahantesh B Walmiki	Design and Implementation of Turbo Codes using VHDL and Estimation of its Error Correction Capability	Prof. M. C. Patel	Dr. M. V. Shah, Professor, L.D. College of Engg., Navrangpura, Ahmedabad
32	P15VL019	Patel Aarti M.	Semi-Custrom Design of Functional Unit Block using Data Path Methodology in Data Cache Unit	Prof. P. K. Shah	Dr. V. S. Tripathi, Asso. Professor, MNIT, Allahabad

| 33 | P15VL020 | Kansara Vismay Sureshbhai | Design and Implementation of Variable Rate Quasi Cyclic Low Density Parity Check Code | Prof. P. J. Engineer | Prof. S. M. Patel, Asst. Prof., IIT, Mandi Kamand, Mandi (HP) |