

PG DISSERTATION (2019-20)

Sr. No	Roll No	Student's Name	Dissertation Title	Guide	External Guide
1	P18EC002	SHUBHAM PANDEY	Design of Food Supply Chain Management System with Middleware Architecture	Dr. U. D. Dalal	Dr. HimanshuSoni, Principal, G H Patel College, VV
2	P18EC003	JAMMIGUMPULLA MANIKUMAR	"Unified coverage driven mechanism for speeding up verification convergence"	Prof. P. K. Shah, Prof. N. B. Kanirkar	Dr. HasmukhbhaiKoringa, Assistant Professor, GEC Rajkot
3	P18EC004	MISTRY PRAVIN TARACHAND	Miniaturization of Slot Antenna using Fractal Shaped Slit Loading	Prof. G. Santra	Dr. J. Sengupta, Assistant Professor, VNIT, Nagpur
4	P18EC005	DEVENDRA KAG	"Speech To Image Translation using Deep Learning "	Dr. Suman Deb, Dr. Kamal Captain	Prof. SamarendraDandapat, Professor, EEE Department, IIT Guwahati.
5	P18EC006	SUHANI			Dr. K. P. Upla
6	P18EC007	NIKHIL DESHMUKH	CPW fed Zeroth Order Resonating Antenna	Prof. G. Santra	Dr. J. Sengupta, Assistant Professor, VNIT, Nagpur
7	P18EC008	AMIT PAL	Design and Performance Analysis of Metamaterial Inspired RF Sensor For Soil Moisture Measurement	Dr. P. N. Patel	Dr. Prabhat Kumar Sharma
8	P18EC009	PRITESH PRAKASH KETHELE	Design of Cold Chain Management System with Middleware Architecture	Dr. U. D. Dalal	Dr. HimanshuSoni, Principal, G H Patel College, VV
9	P18EC010	DHOLAKIA RUCHA SHASHANK	"Dual Band Stub Matched Annular Ring Circularly Polarized Antenna for S-Band and 5G Applications"	Dr. Shilpi Gupta	Dr. R. P. Yadav, Proferssor, MNIT, Jaipur
10	P18EC011	ANEGONDI KRISHNAMA RAJU	DEEP LEARNING BASED CHANNEL ESTIMATION AND SIGNAL DETECTION IN OFDM SYSTEMS	Dr. Shilpi Gupta	Dr. Mangal Singh, Associate Professor, Institute of Technology, Nirma University,dd
11	P18EC012	KANCHAN KUMARI	Sensor Node Design of Primary & Secondary Unit in Master Slave Configuration for IOT Applications	Dr. U. D. Dalal	Dr. HimanshuSoni, Principal, G H Patel College, VV
12	P18EC013	DEEKSHA SINGH	Axial Ratio Tuned Square Aperture Corner Truncated Slot Antenna for Wireless Application	Dr. Shilpi Gupta	Dr. R. P. Yadav, Professor, MNIT, Jaipur
13	P18EC014	AKRITI JAISWAL	"Facial Emotion Detection using Convolutional Neural Network"	Dr. Suman Deb	Prof. SamarendraDandapat, Professor, EEE Department, IIT Guwahati.
14	P18EC016	SARIKHADA RONAK M.	"Speed Up the Time Performance of Validatioin by Advance Formal, Verification Method"	Prof. P. K. Shah, Prof. N. B. Kanirkar	Dr. HasmukhbhaiKoringa, Assistant Professor, GEC Rajkot
15	P18EC017	DHRUV R. BARODIA	Design and Performance Analysis of Angle Interrogation and Wavelength Interrogation Inspired Surface Plasmon Resonance for Sensor Applications	Dr. P. N. Patel	Dr. Yogendra Kumar Prajapati
16	P18EC018	NITIGNA CHAUDHARI	"Landslide Prediction Using NAVIC"	Dr. S. N. Shah	Dr. Nitin Sharma, Assistant Professor, K K Birla Goa Campus.
17	P18VL001	NITIN TYAGI	"DFT and BIST Insertion in SoC and Embedded Processor"	Dr. Z. M. Patel	Dr. R. B. Deshmukh, Professor, Nagpur
18	P18VL003	SWARUPA TUKARAM RAUT	"Clock Distribution Planning for High Frequency"	Prof. P. K. Shah, Prof. P. J. ENGINEER	Dr. HasmukhbhaiKoringa, Assistant Professor, GEC Rajkot
19	P18VL004	RAWAL VIRAJ YOGESHKUMAR	"Hardware Efficient Noise Reduction and Feature Extraction Techniques for Wearable ECG Sensor"	Dr. A. D. Darji	Dr. Virendra Singh, Professor, IIT, Bombay,Powai

20	P18VL005	GAGGUTURU ASHISH	"VLSI Design and Optimization of Programmable DPLL using 180 nm Technology for Bio-Impedance Modulator"	Dr. R. N. Dhavse, Dr. A. D. Darji	Dr. Amit Joshi, Assistant Professor, ECED, MNIT, Jaipur
21	P18VL006	NABAJYOTI KUMAR DAS	"Image Feature Extraction using SIFT on FPGA"	Dr. J. N. Sarvaiya, Prof. P. J. Engineer	Dr. Usha Mehta, Professor, EC Department, Nirma University
22	P18VL007	JIDHIN JOHN	"FPGA Implementation of DDR3 SDRAM Controller with Reduced Latency"	Dr. Z. M. Patel	Dr. Laxshmi B., Associate Professor, NIT Warangal, Telangana
23	P18VL008	NEERAJA ELSA MATHEW	"Hardware Efficient DCT Architecture for HEVC"	Dr. A. D. Darji	Dr. Basant K Mohanty, Associate Dean, Mukesh Patel School of Technology Dhule, Maharashtra
24	P18VL009	PRASHANT PATHAK	"HDI Implementation of LDPC Decoder for CCSDS Standard"	Prof. P.J. Engineer	Prof. Prof. Gaurav Trivedi, Associate Professor, IIT Guwahati
25	P18VL010	RAHUL RANJAN KUMAR	Low Power DCT Architecture for Wireless Capsule Endoscopy	Dr. A. D. Darji	Dr. Basant K Mohanty, Associate Dean, Mukesh Patel School of Technology Dhule, Maharashtra
26	P18VL011	PASUPULA GANGADHAR	"HDL Implementation of Various Blocks in Face Recognition Algorithm"	Prof. P.J. Engineer	Prof. Gaurav Trivedi, Associate Professor, IIT Guwahati
27	P18VL012	ASHWATHI Y.M.	"Low Power RTL Design of DLX Architecture Based RISC Core"	Dr. Z. M. Patel	Dr. R. B. Deshmukh, Professor, Nagpur
28	P18VL013	BYANESHWAR PRASAD RATHORE	"Design of MAC unit for RISC-V based IoT node"	Prof. P.J. Engineer	
29	P18VL014	CHANDRABHANU	"Design of single electron transistor based full adder"	Dr. R. N. Dhavse	Dr. Amit Joshi, Assistant Professor, ECED, MNIT, Jaipur
30	P18VL015	VIGNESH A.	"Design and Implementation of Approximate Arithmetic Based Multiplier for Low Power Applications"	Dr. R. N. Dhavse	Dr. Amit Joshi, Assistant Professor, ECED, MNIT, Jaipur
31	P18VL016	CHITRANIVA KARMAKAR	"Low Power Multipiler Architecture Design for Wearable Bio-Signal Processing Applications"	Dr. A. D. Darji	Dr. Virendra Singh, Professor, IIT, Bombay, Powai
32	P18VL017	HAUDHARI KRUPAKUMAR ANILBHAI	"HDI Implementation of License Plate Extraction Algorithm"	Prof. P.J. Engineer	Prof. Gaurav Trivedi, Associate Professor, IIT Guwahati
33	P18VL018	ASHOKA RAJA Y.M.	"FPGA based display driver design with Remote Terminal Unit(RTU) Support"	Prof. P. K. Shah, Dr. A. D. Darji	Dr. Has mukhbhai Koringa, Assistant Professor, GEC Rajkot
34	P18VL019	CHAVADA MANISH GAUTAMBHAI	"FPGA Implementation of Trigonometric Function Using Cordic Algorithm"	Dr. Z. M. Patel	Dr. Laxshmi B., NIT Warangal, Telangana
35	P17VL015	RATNADEEP PATIL	LDO Regulator with Improved Transient Response	Prof. G. Santra, Dr. Deepak Joshi	Dr. Laxshmi B., NIT Warangal, Telangana